

Amendments to the Drawings:

The attached sheets of drawings are Formal Drawings (Figs. 1-3).

Attachment: Formal Drawings

Title: PROCESS FOR CLEANING AN INTEGRATED CIRCUIT PACKAGE SURFACE, FOR PREPARING THE SAME FOR A SUBSEQUENT INK MARKING PROCESS, AND PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT USING SUCH A CLEANING PROCESS
Inventor(s): Andrea Cigada et al. Express Mail No. EV336611268US Serial No. 09/912,435 Docket No. 854063.654

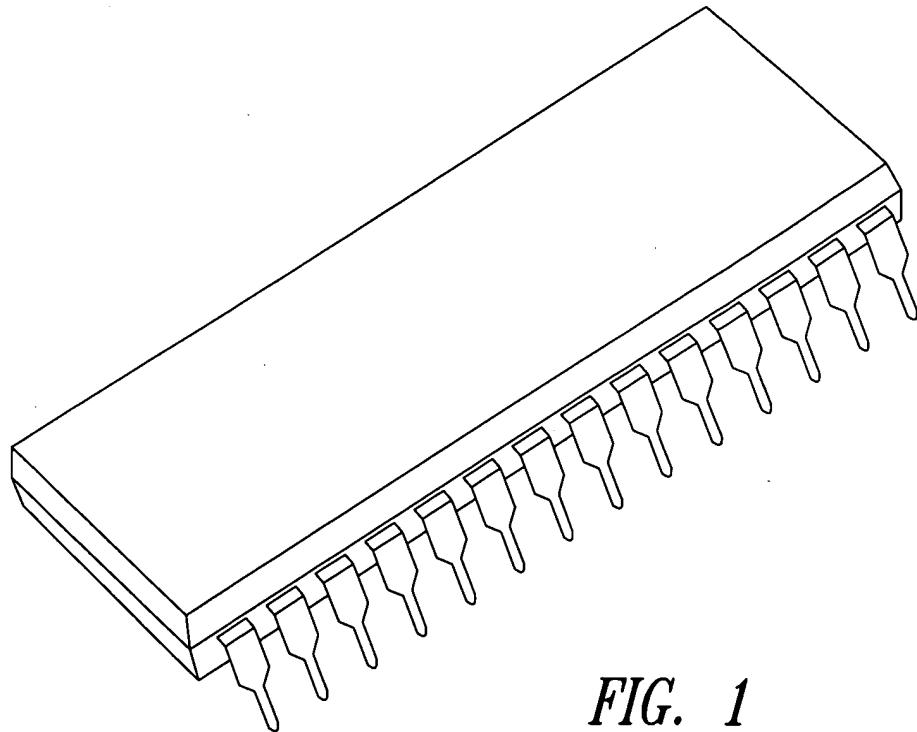


FIG. 1

Title: PROCESS FOR CLEANING AN INTEGRATED CIRCUIT PACKAGE SURFACE, FOR PREPARING THE SAME FOR A SUBSEQUENT INK MARKING PROCESS, AND PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT USING SUCH A CLEANING PROCESS
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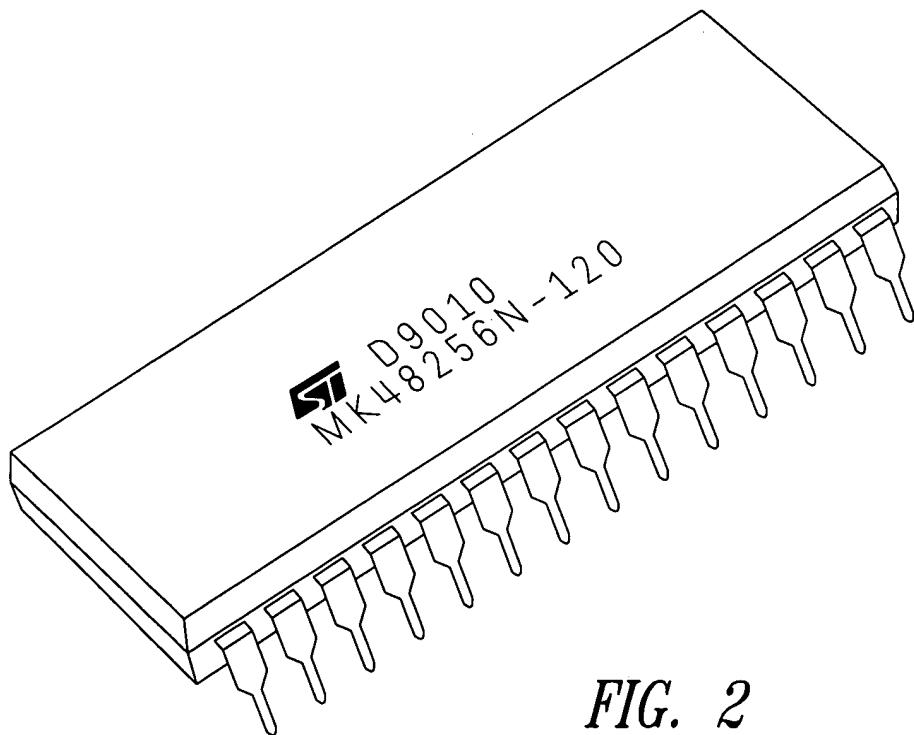


FIG. 2

Title: PROCESS FOR CLEANING AN INTEGRATED CIRCUIT PACKAGE SURFACE, FOR PREPARING THE SAME FOR A SUBSEQUENT INK MARKING PROCESS, AND PROCESS FOR MANUFACTURING AN INTEGRATED CIRCUIT USING SUCH A CLEANING PROCESS
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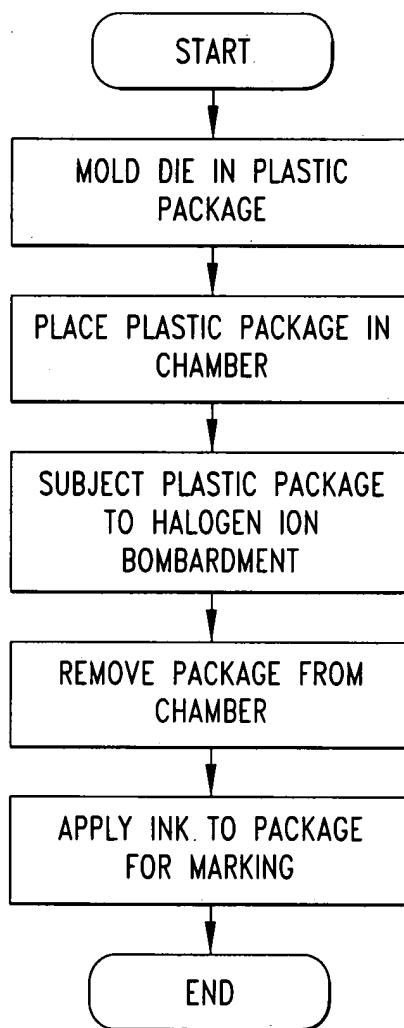


FIG. 3